

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	182	semiconductor and (mold mould) and runner and fabricat\$ and vent	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 11:16
L2	3	semiconductor and (mold mould) and (((corner extended additional end) and inlet) with runner) and fabricat\$ and vent	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 11:20
L3	3	semiconductor and (mold mould) and (((corner extended additional end) and inlet) with runner) and fabricat\$ and (vent vented venting)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 11:20
L4	3	semiconductor and (mold molding mould) and (((corner extended additional end) and inlet) with runner) and fabricat\$ and (vent vented venting)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 11:21
L5	35	semiconductor and (mold molding mould) and ((corner extended additional end) with runner) and fabricat\$ and (vent vented venting)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 11:21